

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/597,194	06/20/2000	Alexander Mostov	TI-29732 6007	
75	590 05/19/2004		EXAMI	NER
Ronald O Nee		ODOM, CURTIS B		
	nts Incorporated	ART UNIT	PAPER NUMBER	
7839 Churchill				
P O Box 65547			2634	
Dallas, TX 75	5265		DATE MAILED: 05/19/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application	on No.	Applicant(s)	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~
	09/597,19	4	MOSTOV ET AL.	
Office Action Summary	Examiner		Art Unit	
	Curtis B.		2634	
The MAILING DATE of this communication a Period for Reply	ppears on the	cover sheet with the c	orrespondence addre	ss
A SHORTENED STATUTORY PERIOD FOR REP THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR 1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a re - If NO period for reply is specified above, the maximum statutory or - Failure to reply within the set or extended period for reply will, by statu Any reply received by the Office later than three months after the mail earned patent term adjustment. See 37 CFR 1.704(b).	l. 1.136(a). In no eve ply within the statu d will apply and wil ute, cause the appli	nt, however, may a reply be tim tory minimum of thirty (30) day: I expire SIX (6) MONTHS from ication to become ABANDONE	nely filed s will be considered timely. the mailing date of this commo D (35 U.S.C. § 133).	unication.
Status				
1)⊠ Responsive to communication(s) filed on 16 2a)□ This action is FINAL. 2b)⊠ Th 3)□ Since this application is in condition for allow closed in accordance with the practice under	nis action is no vance except	for formal matters, pro		erits is
Disposition of Claims				
4) ⊠ Claim(s) 1 and 6-22 is/are pending in the apple 4a) Of the above claim(s) is/are withdrest 5) ⊠ Claim(s) 9,14-17 and 19-22 is/are allowed. 6) ⊠ Claim(s) 1, 6-8, 10-13, and 18 is/are rejecte 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and	rawn from cor d.			
Application Papers				
9) The specification is objected to by the Examination. The drawing(s) filed on 20 June 2000 is/are: Applicant may not request that any objection to the Replacement drawing sheet(s) including the correction. The oath or declaration is objected to by the second secon	a) accepte ne drawing(s) b ection is require	e held in abeyance. See ed if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1	
Priority under 35 U.S.C. § 119				
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the priority docume application from the International Bure * See the attached detailed Office action for a line	nts have bee nts have bee iority docume au (PCT Rule	n received. n received in Applicati ents have been receive e 17.2(a)).	ion No ed in this National Sta	age
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/0 Paper No(s)/Mail Date	18)	4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal F 6) Other:		(2)

Art Unit: 2634

DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1, 6-8, 10-13, and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ong (U. S. Patent No. 5, 490, 175).

Regarding claim 1, Ong discloses a communications receiver comprising:

an LNA (Fig. 1, block 112, wherein the RF amplifier is a low noise amplifier) for amplifying a received signal so as to generate an LNA output signal, the LNA having M gain setting modes of operation (column 2, lines 59-64), the gain of the LNA determined in response to an LNA gain control command (column 2, lines 55-59);

a mixer (Fig. 1, block 114) for multiplying the LNA output signal so as to generate a mixer output signal, the mixer having N gain setting modes of operation (Fig. 4, column 3, line 65-column 4, line 13), the gain of the mixer determined in response to a mixer gain control command signal (column 2, lines 55-59);

a detector (Fig. 1, block 140) for recovering from the mixer output signal, information originally transmitted;

Application/Control Number: 09/597,194 Page 3

Art Unit: 2634

a controller (Fig. 1, block 106, column 2, line 55-column 4, line 19) to generate the gain control command to the LNA and the mixer gain control command to the mixer, the controller adapted to set the setting of the LNA and of the mixer to one of a plurality of gain states wherein each gain state consists of a unique combination of LNA gain settings and mixer gain settings (Fig. 4, column 3, line 65-column 4, line 13).

Ong does not disclose the mixer multiplies the LNA output signal with a local oscillator signal. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made that multiplying a received signal by an oscillating signal is an operation which is used simply convert the received signal to a desired frequency. Thus, if the signal is already received at a desired frequency, there is no need to implement a local oscillator. Thus, converting the signal to a desired frequency is deemed a design choice and does not constitute patentability.

Regarding claim 6, which inherits the limitations of claim 1, Ong does not disclose a band pass filter located before the LNA and adapted to filter the signal received from the antenna. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to include this feature to remove spurious signals which can cause intermodulation products and ultimately result in a loss of information.

Regarding claim 7, which inherits the limitations of claim 1, Ong does not disclose a band pass filter located before the mixer and adapted to filter the LNA output signal before input to the mixer. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to include this feature to remove spurious signals which can cause intermodulation products and ultimately result in a loss of information.

Art Unit: 2634

Regarding claim 8, which inherits the limitations of claim 1, Ong does not disclose a band pass filter located after the mixer and adapted to filter the mixer output. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to include this feature to remove spurious signals which can cause intermodulation products and ultimately result in a loss of information.

Regarding claim 10, Ong discloses a communications receiver comprising:

an LNA (Fig. 1, block 112, wherein the RF amplifier is a low noise amplifier) for amplifying a received signal so as to generate an LNA output signal, the LNA having a low gain and high gain mode of operation (column 2, lines 59-64 and Fig. 4, column 3, line 65-column 4, line 13), the gain of the LNA determined in response to an LNA gain control command (column 2, lines 55-59);

a mixer (Fig. 1, block 114) for multiplying the LNA output signal so as to generate a mixer output signal, the mixer having a low gain and high gain mode of operation (Fig. 4, column 3, line 65-column 4, line 13), the gain of the mixer determined in response to a mixer gain control command signal (column 2, lines 55-59);

a detector (Fig. 1, block 140) for recovering from the mixer output signal, information originally transmitted;

a controller (Fig. 1, block 106, column 2, line 55-column 4, line 19) to generate the gain control command to the LNA and the mixer gain control command to the mixer, the controller adapted to set the setting of the LNA and of the mixer to one of a four gain states (Fig. 4) wherein each gain state consists of a unique combination of LNA gain settings and mixer gain settings (Fig. 4, column 3, line 65-column 4, line 13).

Ong does not disclose the mixer multiplies the LNA output signal with a local oscillator signal. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made that multiplying a received signal by an oscillating signal is an operation which is used simply convert the received signal to a desired frequency. Thus, if the signal is already received at a desired frequency, there is no need to implement a local oscillator. Thus, converting the signal to a desired frequency is deemed a design choice and does not constitute patentability.

Regarding claim 11, which inherits the limitations of claim 10, Ong does not disclose a band pass filter located before the LNA and adapted to filter the signal received from the antenna. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to include this feature to remove spurious signals which can cause intermodulation products and ultimately result in a loss of information.

Regarding claim 12, which inherits the limitations of claim 10, Ong does not disclose a band pass filter located before the mixer and adapted to filter the LNA output signal before input to the mixer. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to include this feature to remove spurious signals which can cause intermodulation products and ultimately result in a loss of information.

Regarding claim 13, which inherits the limitations of claim 10, Ong does not disclose a band pass filter located after the mixer and adapted to filter the mixer output. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to include this feature to remove spurious signals which can cause intermodulation products and ultimately result in a loss of information.

Art Unit: 2634

Regarding claim 18, Ong discloses a communications receiver comprising:

an LNA (Fig. 1, block 112, wherein the RF amplifier is a low noise amplifier) for amplifying a received signal so as to generate an LNA output signal, the LNA having a low gain and high gain mode of operation (column 2, lines 59-64 and Fig. 4, column 3, line 65-column 4, line 13), the gain of the LNA determined in response to an LNA gain control command (column 2, lines 55-59);

a mixer (Fig. 1, block 114) for multiplying the LNA output signal so as to generate a mixer output signal, the mixer having a low gain and high gain mode of operation (Fig. 4, column 3, line 65-column 4, line 13), the gain of the mixer determined in response to a mixer gain control command signal (column 2, lines 55-59);

a detector (Fig. 1, block 140) for recovering from the mixer output signal, information originally transmitted;

a controller (Fig. 1, block 106, column 2, line 55-column 4, line 19) to generate the gain control command and the mixer gain control command, the controller operative to set the LNA to high gain mode the mixer to high gain mode to achieve maximum sensitivity (Fig. 4, Set to HIGHEST gain, column 3, line 65-column 4, line 13).

Ong does not disclose the mixer multiplies the LNA output signal with a local oscillator signal. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made that multiplying a received signal by an oscillating signal is an operation which is used simply convert the received signal to a desired frequency. Thus, if the signal is already received at a desired frequency, there is no need to implement a local oscillator. Thus,

Application/Control Number: 09/597,194 Page 7

Art Unit: 2634

converting the signal to a desired frequency is deemed a design choice and does not constitute patentability.

Allowable Subject Matter

3. Claims 9, 14, and 15 are allowable over prior art because related references do not disclose setting the LNA and mixer to different gain modes using a controller, including a detector which comprises of a limiter, discriminator, and data slicer. Claims 16, 17, and 19-22 are allowable over prior art because prior art references do not disclose setting the LNA and mixer to different gain modes to improve linearity while reducing sensitivity and a controller which switches to a state having a lower sensitivity in response to a low correlation; a controller which switches the receiver back to a previous state if the error rate obtained in a new state is worse than the error rate in a previous state; and a controller which sets the receiver to a low gain state in response to a high RSSI reading and a high error rate.

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Curtis B. Odom whose telephone number is 703-305-4097. The examiner can normally be reached on Monday- Friday, 8-5.

Art Unit: 2634

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Stephen Chin can be reached on 703-305-4714. The fax phone number for the

Page 8

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Curtis Odom May 13, 2004

SUPERVISORY PATENT EXAMINE
TECHNOLOGY CENTER 2600